INTERNATIONAL MINIMUM REQUIREMENTS

FOR

ELECTRONIC CLOCKING SYSTEMS

By: - FédérationColombophile Internationale
       - Manufacturers Electronic Clocking Systems and chiprings
       - Testing Agencies

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Contents

Introduction

ThisInternational Minimum Requirements document has been composed for member countries of FCI (FédérationColombophile Internationale)and manucturers, to set the minimum requirements for the electronic clocking systems, in order to help improve the integrity of software and hardware of racing pigeon clocking systems.

Specifically the requirements for systems and chiprings must ensure that system functionality cannot easily be compromised and that unauthorized tampering with system components will be detected or discovered.

The requirements consist of assessment regulations, the international standard for electronic clocking systems, security requirements for electronic clocking systems and demands for the performance of footrings, combi-rings and electronic chiprings.

The requirements are composed and supported by FCI for Electronic Clocking Systems, the praesidiums of FCI-member countries, the manufacturers of Electronic Clocking Systems and Testing Agencies.

1. <u>Glossary of Terms</u>

These pages aim to introduce new terms and clarify controversial ones regarding the document 'International Minimum Requirements for Electronic Clocking Systems'.

1.1 Chipring or electronic ring	A ring, provided with an integrated electronic chip which contains identification (Chip Number) - and additonal user-data that can, in accordance with requirements, be read and written electronically.
1.2 Combi-ring	A combi-ring combines the footring- and chipring-functions by integrating an interchangeable, electronic chipunit with the footring. The chipunit contains the chip with the identification (Chip Number) - and additional user-data for electronic clocking.
1.3 Copy ring	The term used instead of cloned rings.
1.4 Custom-ID	The custom-ID is the first 8 bits of the Chip Number and is a reference to the type of the chip and the manufacturer of the chip. Every manufacturer has an own dedicated custom-ID, that is unique. Custom-ID's are managed and assigned by the FCI. With the custom-ID, every manufacturer has a separate number range to prevent duplicated ring ID's on the market (copy rings). No manufacturer is allowed to use a custom-ID that had already been assigned to another manufacturer.
1.5 Declaration of responsibility	Previously referred to as 'anti-fraud guarantee'. Because pigeon federations and system developers have the same interest in preventing fraud, the system developer should give a kind of declaration of responsibility to individual federations.
1.6 ET	Electronic Timer, also referred to as 'electronic clock', 'pigeon clock' or 'fancier clock'. Fancier's data, pigeon data and race results (arrival times) are stored in the clock's memory.
1.7 ETS	Electronic Timer System. Also referred to as Electronic (racing pigeon) Clocking System.
1.8 Footring	A footring is a ring which, once put on a pigeon's leg, serves as an identification of the pigeon, gathered from a clearly readable number, the footring number.
1.9 Homologation procedure	ETS manufacturers cooperating with FCI get an FCI-certificate of homologation. FCI keeps a list of all approved ETS on the market. (The approval procedure is explained in the material 'International Minimum Requirements for Electronic Clocking Systems'.)

1.10 Investigation

Target of the investigation for acceptance (hereafter mentioned: investigation) is to verify that a system is able to meet the requirements to electronic clocking systems. The Testing Agency will handle the Assessment Regulations and related requirements as a basis for the investigation of a system to be carried out. An investigation may only be carried out in accordance with the quotation, based on the availability of all necessary materials and documentation of the system, for which the applicant has requested a certificate.

1.11 Linking

Linking (or register) is the operation where the federation's band numbers are referenced (linked or coupled) to the electronic ring ID's. Linking can be described as editing the fancier and pigeons data in the fancier clock.

1.12 Procedures in the Club

Protocols for basketing, read out, result processing and linking; serving as a guide to fanciers on how to check the basics.

1.13 Project dossier

For each request for an investigation a dossier will be set up. The dossier will contain all information in relation to the request and its treatment by the Testing Agency. The information will be saved at least for 5 years on behalf of eventual complaints or arbitration.

1.14 Read out

When the pigeons are home the fancier clock has to be returned to the club to print the clocking (arrival) list. The first important action is synchronizing the master timer (HKW, GPS, master clock). Synchronizing the master timer must be done by a commission(more than one person).

1.15 Reporting

The report from the Testing Agency given at the end of the testing process will comprise the following elements: characteristics of the tested product, requirements and criteria (limiting values) used for comparison, testing- and measuring results, comparison of the results, Declaration of Conformity with signature and other relevant notes.

1.16 Request

When a manufacturer has sent a request for the (continuation of) certification of a product to FCI and this request was accepted by FCI, FCI will send an application for an assessment by an FCI-acknowledged, independent office chosen by the manufacturer, hereafter named: Testing Agency. The manufacturer or applicant provides FCI with the necessary information about the product to be certified, so as to enable FCI to come to a right judgement of the request to certification.

1.17 RFID

Radio-frequency identification (RFID) is the use of an object (typically referred to as an RFID tag or transponder) applied to or incorporated into a product, animal, or person for the purpose of identification and tracking using radio waves.

2. Assessment regulations

<u>Contents</u>

<u>Introduction</u>

The increasing complexity of products (the electronic pigeon-identification systems and chiprings) and growing distance between manufacturer and user (the fancier) brings forward the need of quality guarantee and a certificate.A certificate proves the existence of a justified confidence that electronic clocking systems and chiprings are delivered according to Assessment regulations as referred to in the certificate.
Issuing (product)certificates for electronic clocking systems and chiprings is internationally reservedto a dedicatedFCI-committee. The FCI-committee actsas an independent, corporate body of at least three persons from different institutes. They decide about the order for an assessment and certification of a system or chipring on basis of a report of the testing agency. The assessment, carried out by the testing agency, takes placeaccording the assessment regulations given in this chapter.

2.1 Request for a certificate

When a manufacturer has sent a request for the (continuation of) certification of a product to FCI (or its certification body) and this request was accepted by FCI, FCIwill send an application for an assessment by an FCI-acknowledged, independent bureau, hereafter named: TestingAgency.

At a request to certification, FCI sends the manufacturera query and provide the necessary information, incorporating these Assessment Regulations. If requested by the manufacturer and with the permission of FCI, the Testing Agency may directly provide the Assessment Regulations to the manufacturer.

The manufactureror applicant provides FCI with the necessary information about the product to be certified, so as to enable FCI to come to a right judgement of the request to certification.

The applicant may send the necessary information for the request to certification directly to the Testing Agency. The latter will then inform FCI about the request for an assessment.

The Testing Agency, when requested by FCI, will make a quotation to FCI, with statement about cost for the requested investigation.

The order for the investigation (or additional investigation) to the Testing Agency will be given by FCI.

The quotation of the Testing Agency will mention the activities to be carried out, the contact person, terms of delivery and planning information, in order to bring forth a declaration of conformity.

The Testing Agency is essentially due to consider all requests for certification. When serious arguments arise against further considering a request, the Testing Agency will contact FCI about this.

2.2 The investigation

Target of the investigation for acceptance (hereafter mentioned: investigation) is to verify that a system is able to meet the requirements for racing pigeon clocking systems.

The Testing Agency will handle these Assessment Regulations and related requirements (in Annex) as a basis for the investigation of a system to be carried out.

An investigation may only be carried out in accordance with the quotation, based on the availability of all necessary materials and documentation of the system, for which the applicant has requested a certificate. The applicant may not do with just delivering the system or system components.

The necessarymaterials are:
- A working system and related hardware, necessary for the tests.
- Design documentation of the hardware: housing and electronic components;
- Components list(s);
- Design documentation of the software (flowcharts) of security functions (data protection mechanisms);
- Description of storage of fraud-sensitive data (flight data, software, keys);
- Descriptions of algorithms andkey management to go with cryptographic security (if applied).

An investigation is quoted on request by FCI. The offered work will comprise:
- Study of thedocumentation
- Functional tests of a working system or component
- Physical tests and verifications
- Testings to requirements
- Correspondence and discussion with the applicant, when necessary
- Reporting in Dutch and English language.

The quotation of the Testing Agency will be valid for two months.

At positive result of an investigation, a test report will be generated to FCI within 6 weeks after start of the investigation.This report contains descriptions of the product, the carried out tests, test results, used materials and conclusions.
A signed Declaration of Conformity will be added in a separate annex to the test report.

The delivery terms of the test report may be adapted when during the investigation delay in testings occur, for example owing to system defects.

When during an investigation the (interim) results obviously show that the investigation will not lead to a declaration of conformity or, respectively, an approval, the Testing Agency will immediately inform the manufacturer and FCI about this and will, until further notice, stop the investigation.

If the applicant or FCI, after disclosure of negative (interim) results, or for other well-founded reasons does not want to continue the investigation, the cost for testing made so far will be charged.

Continuation of an interrupted investigation can take place within 2 months after the investigation was arrested and after the manufacturer has made clear that measures have been taken to improve the product concerning the crucial points that had led to the negative (interim) results.
The applicant will then make a new sample of the product or the system available and if applicable, adapted or added documentation.

When continuation of the investigation, for example, due to expiration of the abovementionedtime-limit of 2 months, needs re-testings to be carried out, the Testing Agency will offer the extra work to FCI. The investigation will be continued or re-started in consultation with the applicant and FCI.

At a negative final or interim result of the investigation there will in principle no official test report be generated. However, on request from the applicant the carried-out tests and results can be reported, but a declaration of conformity will not follow.
After reporting the total cost of the investigation as quoted will be charged

Comparison of the test results will allways take place to the latest version of the agreed requirements as attached to these Assessment Regulations.

Only a positive result of the tests and comparison to the requirements may lead to a signed declaration of conformity of the Testing Agency.

2.3   Reporting

The report from the Testing Agency will comprise the following elements:
a.   Title page:
   - Report number, date and copy number
   - Product description
   - Type- and trade-mark of the product
   - Name and address of contractor
   - Name and address of manufacturer / deliverer of the product
   - Author of the report and signature
b.   The requirements and criteria (limiting values) used for comparison
c.   The testing- and measuring results
d.   Comparison of the results
e.   Declaration of Conformity with signature
f.   Other relevant notes

The reports should be set up in a distinguished style of the Testing Agency.

In case the requirements used for the tests leave room for interpretation, it will be explained in the report under what conditions the Testing Agency has come to a positive conclusion.

The Testing Agency will add in the report a declarationthat he has never been involved in the development of the concerned product.

The report copy numbers 1 and 2 will be sent to FCI.
Copy nr. 1 is intended for FCI, copy nr. 2 may be put available by FCI to the applicant.
Copy nr. 3 will be kept in the project dossier of the Testing Agency.

2.4 Project dossier

For each request for an investigation a dossier will be set up. The dossier will contain all information in relation to the request and its treatment by the Testing Agency.

The information will be saved at least for 5 years on behalf of eventual complaints or arbitration. The contents of the dossier will be handled as 'Secret' for unauthorized.

The Testing Agency may only provide information to persons with interest about the fact that a product is either certified by FCI or not. For that purpose, a list of certified products will be kept up to date.

Documents provided by an applicant, which are not of importance to be stored, will be marked and sent back to the applicant.

The dossier contains the following distinguished parts:
1. Continuation scheme of the investigation.
2. Request, order, copy (no. 3) of the test report, declaration of conformity and FCI-certificate.
3. Notes, etc., made within the scope of the investigation
4. Financial administration: quotation, project form, invoice, etc.

3. International Standard for Electronic Clocking Systems

Contents

Introduction

Basics
All known ETS are using Radio Frequency Identification techniques (RFID) to clock racing pigeons. They all operate in the low frequency (LF) area at 125KHz with passive transponders.
Radio-frequency identification (RFID) is the use of an object (typically referred to as an RFID tag or transponder) applied to or incorporated into a product, animal, or person for the purpose of identification and tracking using radio waves. Some tags can be read from several meters away and beyond the line of sight of the reader.

RFID tags/transponders
An integrated circuit (chip) for storing and processing information, modulating and demodulating a radio-frequency (RF) signal, and other specialized functions.
The second is an antenna (coil) with capacitor for receiving and transmitting the signal.
There are generally three types of RFID tags: active RFID tags, which contain a battery and can transmit signals autonomously, passive RFID tags, which have no battery and require an external source to provoke signal transmission, battery assisted passive which require an external source to wake up but have significant higher forward link capability providing great read range.
RFID transponders operate in a specific electro-magnetic field and are in general classified as: 125/133KHz (Low Frequency LF), 13.56MHz (High Frequency HF), 860 and 950 MHz (Ultra High Frequency UHF), 2.45GHz (Microwave MW). Higher frequencies are used to get faster readings. However they have more problems with metal and liquids than the lower frequencies.
RFID is used in several applications:
race timing (pigeons, athletics, …),passports,mobile payments,product tracking,animal identification,inventory systems, …
Transponders used by ETS are from the LF passive type, they get their energy from an external electromagnetic field. ETS uses transponders of a read-write type, meaning that information also can be read from and written to the transponder.

RFID readers/writers used for passive transponders
The reader presents an electromagnetic field that excites the coil and charges the capacitor, which in turn energizes and powers the chip. The IC then transmits the identification number (ID) via the coil to the reader. The readers communicate in a binary protocol (0 or 1 circuit).
In general readers have their antenna and processing unit together in the same housing.
ETS readers have these parts separated. The processing unit is mostly referred to as the fancier's clock or the electronic timer (ET). The antenna part is referred to as loft-antenna or club-antenna.

Copying RFID transponders
With the appropriate device and little knowledge it's fairly easy to copy a transponder. Just read its ID and write it to a duplicate transponder. The internet provides everybody with the knowledge and advertises the devices.

Hard- and software securing the integrity/reliability of the clocking data
While calculating a pigeons' result list, the timing and identification of the individual pigeon is a crucial element regarding the integrity of the calculated average speed. As long as pigeon races are organized both issues (identity and timing) have been subject of many rules and protocols to ensure the integrity of the race result.
It's a general misunderstanding amongst pigeon fanciers that cheating is impossible with ETS, it might be more difficult but not impossible. In certain situations it could be even easier because the specialized electronic systems create a false feeling of safety. This false feeling of safety can create an environment where rigorous checking disappears.

3.1   Security precautions

100% security is impossible and therefore cannot be demanded. In the fight against fraud ETS manufacturers and pigeon federations should join hands. The integrity of the data can only be guaranteed if the hard- and software in the clocks together with the protocol in the clubs at basketing and readout enhances the risk of being caught for a potential cheater.
Any attempt can only be successful if:
- The costs are far less than the gain,
- The attack leaves no traceable marks,
- The system continues to function normally after the attempt.
The following issues should get attention:
- Hardware used by the fancier and in the club,
- Software used by the fancier and in the club,
- Linking, basketing and readout protocol set by individual federations following FCI guidelines.
Before discussing any safety/security issues, one needs to understand how a RFID/ETS system works. This basic knowledge combined with the know-how of organizing pigeon races should result in a document which could be the base for each federation to establish its rules in such a way that reliable race results are produced.

3.2   ETS components (hardware)

Components of an ETS can be divided in to 2 groups:
- Components managed by the fancier (fancier's terminal (Electronic Timer - ET), loft-antenna, electronic ring (ER), interfaces …)
- Components managed by the club (basketing unit, interfaces, master timer, pc …)

3.2.1 Electronic timer, loft antenna, electronic ring, interfaces

The electronic ring (ER)
All known ETS use a passive 125KHz LF transponder for identifying individual pigeons. They

all have a 64-bit section which is communicated spontaneously in a H4001 (ref. 1) norm to the reader antenna.

The loft antennas.
Almost all loft antennas used by ETS can be described as read-only, they can only read the 64-bit data stream (ID) which is send spontaneously when an ER is presented in the field of the loft antenna. These loft antennas can't read the additional information stored in an ER neither can they write information to the ER. They only register the spontaneously emitted ID from the ER and communicate this to the fancier's ET.

Fancier's terminal or electronic clock /timer (ET).
The ET is in general equipped with:
- a microprocessor,
- real time clock (RTC),
- communication ports.

It's the ET which stores all kind of data, fancier's data, pigeon table. The pigeon table is a list which references the ID of the transponder to an individual pigeon number in the clock.
The RTC is the reference for the time-stamps stored in the pigeon table for individual pigeons clocked.
Communication ports are the gates trough which the ET communicates with the loft antennas, the club unit, pc, interfaces, …

Interfaces.
Several interfaces can be mounted to the ET to enhance its functionality and connection features. I.e. : printer, pc, mobile phone, bluetooth, wireless lan, ethernet, …

3.2.2 Club-antenna, club-interfaces, master-timer, pc …

Club antenna / Club unit.
Club antennas are comparable with loft antennas with read/write facilities. Beside the 64-bit ID of the ER they can also read and write the additional information stored in the ER. Club antennas take an important place in the security management of a pigeon race.

Club-Interfaces.
Interfaces provide a wide range of communication possibilities between the club-antenna several peripherals as there are: Electronic Timer (ET), Master Timer, printer, pc, Ethernet, protocol convertors …

Master-Timer.
Is a real time clock used as a reference timer for the complete pigeon race. Mostly used is the DCF 77 (HKW radio-clock) or a GPS-receiver.

Personal computer (pc).
A pc is mostly used to upload the pigeon table in the ET and for the result processing.
All these components are managed by the club, they are used for linking, basketing, readout and result processing.

3.3   ETS software

The software is the driving power of any ETS system. Software is referring to the software in the several components used while basketing, clocking and readout. (ET, club unit, interfaces, protocol convertors, master-timer, pc, …)
Software also has an important role in the security management of a pigeon race.
Software must be adjusted by the member country it is applied in.

3.4   Security criteria – ETS hardware

As stated before 100% security is impossible (§1). Because pigeon federations and system developers have the same interest in preventing fraud, the system developer should give a kind of declaration of responsibility to individual federations. This declaration is difficult to give in general (i.e. FCI) because individual federations have specific demands which might have an impact on the security of the system.

3.4.1 Electronic ring (ER)

The ER has an important role in the fight against fraud. It's the ID linked to the pigeon in the clock's pigeon table. Nobody can guarantee that the ring's ID is unique, transponders can be copied. Because of this the ER should get a new ID each time the pigeon is basketed. This ID has to be generated or initiated by the club antenna / club unit and has to be stored. The Electronic Ring ID has to be unique for each manufacturer.
Because pigeon clocking only involves the spontaneously send 64-bit data stream, only this section has to get our main interest.
The 64-bit data stream only has 40 bits usable for identification. These 40 bits are divided in to 2 sections: a fix part containing the ID of the pigeon and a changeable part containing the new ID which changes at each basketing.
        - Fixed part: 32 bits where the first 8 represent the custom-ID.
        - Changeable part: 8 bits regenerated at each basketing (secret code).
The custom ID is the first 8 bits of the chip number (D00-D03...D10-D13 ref. 1.) is a reference to the type of the chip and the manufacturer of the chip.
Every manufacturer has an own dedicated custom ID, that is unique. Custom ID's are managed and assigned by the FCI. With the custom ID, every manufacturer has a separate number range to prevent duplicated ring ID's on the market (copied rings). No manufacturer is allowed to use a custom ID that had already been assigned to another manufacturer.
The 8 bits secret code offers a sufficient tool against the use of copied rings, these 8 bits can generate 256 different codes. So a possible ring copy only has 1 on 256 chances of being successful. It might look that this is not sufficient, but cheating is mostly done systematic and repeated. With 1 copied ring the chance is 1 on 256, for 2 copies of the same ER the chance is 1 on 65.536, 3 copies only offer 1 chance on 16.777.216 possibilities.
The next 24 bits (D20-D23 .. D70..D73 ref. 1) containing the fix ID of the pigeon, leaving 16.777.216 combinations for each custom-ID.
A list of chip types actually used in pigeon transponders and complying with the abovementioned features:
- P4165 from AEG manufactured by Electro Marin, secret code generated on chip,
- Nova from and manufactured by Sokymat, secret code generated in the club unit,
- Hitag-II from and manufactured by NXP, secret code generated in the club unit,

- Hitag-S from and manufactured by NXP, secret code generated in the club unit.
Nowadays, the FCI concentrates on Hitag-S ringsonly.
There where the club unit has to generate the secret code specific precautions have to be taken in on how to generate the secret code in the transponder. These issues are explained in more detail in the paragraph 4.2. regarding the club antenna / unit.
The P4165 is actually used in Germany as a universal ring under the name Tipes600+, Tauris600+ and Bricon 3000+ are also based on this chip type.
The Nova chip is only used in the MC601 from Motz-Computer (Tipes).
The Hitag-II is used by the companies Atis-Benzing, Tauris and Unikon.
The Hitag-S is used by the companies Atis-Benzing, Bricon, Motz-Computer (Tipes), Tauris,Unikonand later manufacturers [ref.: Reports Overview].
Clocking pigeons only involves the 64-bits of data spontaneously emitted when the transponder enters the antenna field (loft antenna). Each of the above listed transponders has extra memory available to store additional data. These data are insignificant for clocking although they can add an extra dimension to the use of the ER. One has to take into consideration that each chip type processes these data differently and therefore limits the possibilities. Mostly the extra data contains the license number of the fancier issued by the federation with a country-ID. Also, extra memory can be used for security purpose. For example, a chipring vendor can apply coded data, which enables him to detect copied rings.

3.4.2 Club-antenna /-unit

The most important feature in the fight against fraud is the secret code initiation or generationat basketing. Every club-unit must have the ability to complete this task.
The system has to generate the secret code in such a way that it's impossible to predict the outcome of the process.The system cannot produce any external sign from which the outcome of the process can be predicted.
For the chip types which donot have an on-chip secret code generator, the secret code is generated by the club-antenna. The generated code is then written in the chipring and stored in the fancier's Electronic Timer.
The wireless transmission of data between chipringand club-antenna, should be of such a nature that unauthorized tapping of the signal is very hard or impossible from a distance of more than 1 m.

3.4.3 Fancier clock

The fancier clock has in general 4 different parts: processor, memory, real time clock (RTC) and one or several communication gates and operating means (display, keypad). Several of these parts might be combined in to one piece of hardware.
Fancier's data, race data and pigeon data are stored the clock's memory. The format of the fancier's data are set by the individual federations. The fancier's record can contain at least the following fields : <License number >, <Name >, <Address >, <Postal code >, <Community/City >, <XX-coordinate >,<YY-coordinate > .
Pigeon data are stored in a pigeon table, each record of this pigeon table can contain the following fields: <record number >, <country >, <year >, <federation ring >,<electronic code >, <secret code >, <pigeon status >. Depending the rules of the individual pigeon federationsome extra fields can be added to each pigeon record i.e. <color>.

Each race record can contain the following fields: <race code >, <club number >, <internal date at basketing>, <master timer date at basketing>, <internal time at basketing>, <internal date at readout >, <master timer date at readout >, <internal time at readout > , <master timer time at readout > and <race status >. All these data can be stored in a compressed mode, the record lengths are only indicating the length of the output. The hardware should provide the necessary components to store these data in a save way.

Pigeon and race records should be stored in memory in such a way that unauthorized access is impossible. While the fancier clock has active races on board it should be impossible to change any of these data unless as foreseen by the federation's protocol.

The fancier clock should be protected against unauthorized access by the housing. The housing concept should be in such a form that an unauthorized entry leaves visible marks. The housing should be sealed in such a way that the seal or the housing is destroyed in such a way that the seal can't be replaced without visible traces. It must be hard to produce a new seal without the intervention of the system's manufacturer.

The layout and concept of the fancier clock should be of such a nature that it resist or shows any attack with the following energy forms:

- mechanical,
- electrical (i.e. trough I/O ports),
- magnetic,
- chemical (i.e. solvents),
- electromagnetic (light, UV, IR, …),
- thermal energy (cold, heath).

The layout of the internal motherboard should be in such a way that critical parts like the microprocessor, the memory and the RTC can't be attacked easily through small openings (hidden) in the housing. The layout should be of such a nature that any unauthorized editing or retrieval of data is very difficult or impossible.

The concept of the RTC should be such that the oscillator cannot be influenced from outside (i.e. influencing the crystal). The RTC is very crucial, the clocking times are derived from the RTC.

A second layer of protection against unauthorized access has to be provided by the software.

3.4.4 Master timer

The master timer is the reference timer for each race. If several clubs (basketing stations) are competing in the same race one should use a system that guarantees the same time settings in each club. Before starting a basketing session the time of the master timer should be checked against a independent public timer. It's the club's responsibility to check that the master timer gives the correct time and date.

The following hardware is accepted:

- radio signal devices or HKW radio-clock, if the reception is checked.
- GPS module, if the time is checked.
- Masterclock set by one of the above or set manually and properly checked.

Any of these peripherals should show the time. In 2 timers the time must be displayed at any moment for cross-checking with the independent public timer and the timer of the fancier's terminal (ET). Reference time shall be shown on the Club System.

Manual setting of individual clocks should be discouraged, the inaccuracy of manual setting could doubt the integrity of the race result.

3.4.5 Interfaces at the club

These interfaces are the communication gates between several peripherals, i.e.: printer, pc, master timer, club-antenna, fancier's terminal, protocol convertors, …
These interfaces can be separate parts or better they are incorporated in the club-antenna/ unit. To connect the different parts used in the club environment, a connection with a galvanic nature (wire) is preferred. Wire connections can easily be checked for unauthorized tapping of information, provided that these are under strict control by club personnel.
When using a wireless data transmission between club devices (i.e.: Bluetooth, IR, RF, except chiprings), at least the data streams containing time synchronization and clocking data should be cryptographicallyprotected.The cryptographic keys should be stored in such a way that they are not accessible for unauthorized users.

3.4.6 Loft antennas

Loft antennas are mostly read-only, they can only read the spontaneous emitted data send by the electronic ring when it enters the antenna field.Only these data is sent to the ET for clocking, together with provisions to assure integrity of the arrival data.
The reading distance of a loft antenna in combination with an authorized electronic ring should be maximum approx. 20cm.
The position of the loft antenna is also very important for the integrity of the race result. The loft antenna should be inside the loft or when placed outside it should be near the pigeon entry. The distance from the outer border of the antenna to the center of the entry should be limited, 1m might be a good average. An individual federation can decide in this matter, FCI should set a general recommendation.

3.4.7 Interfaces managed by the fancier

Just like in the club these interface are add-ons providing communication gates with several peripherals, i.e.: printer, pc, mobile phone, internet, protocol convertors, …
Every manufacturer has to guarantee that these interfaces cannot be used by unauthorized users to edit or delete critical data in the electronic timer (ET) when a race is active.
These interfaces can only be used for downloading data to other device or platforms. Never can they be used to get information about the secret codes in theET and electronic rings.

3.4.8 Continuous clocking unit

A continuously clocking system is a device or interface which registers and clocks pigeons while the main timer is not available (out for basketing, readout, …).
- A continuous clocking unit should be equipped with its own real-time clock (RTC)for time (interval) registration.
- The continuous clocking device has to comply with the same security standard as in the fancier's terminal (ET).
- When the device is a copy-clock, it is taken to the club with the ET for basketing and evaluation. The device is then connected to the ET during basketing.
- The continuous clocking unit is charged with a high risk factor. The use of such a unit must be subject to strict safety and protocol requirements. These requirements must be

covered mainly by the connection and synchronization protocol between the devices. This is covered in section 3.5: 'Security criteria – software'.

3.5   Security criteria - software

The software driving the individual components of an electronic timing system is very crucial for the integrity and reliability of the result list.
The most critical parts are the fancier's terminal or electronic timer (ET) and the club-antenna or club-unit.

3.5.1 The fancier's terminal or Electronic Timer (ET)

The Basic Input Output System (BIOS) must be designed so that any unauthorized software upload blocks the system. Software should be protected.
The memory, storingthe software and all critical race and pigeon data, should be locked so that unauthorized reading is impossible. The serial number of the ET is also seen as critical and therefore has to be stored so that it cannot be edited by unauthorized users.
- Reading or displaying the secret code of any individual pigeon should be impossible at any time.When a pigeon (competing in an active race) is clocked with a wrong secret code, the software should display this pigeon on the clocking list as 'Not OK', disqualifying this pigeon as such from the result. This status may not be overruled by consecutive clocking of the same bird (only one go).
- The software should separate 'Club mode' and 'Loft mode' operations of the ET.
Club mode:
- The real time clock (RTC) can only be synchronized against a master timer for the first active race. This is the first race which started after the clock was read and deleted. All races, started with an active race present, cannot synchronize the RTC again. A relative time table has then to be built, keeping track of the deviations of the RTC against the master timer for this race. Synchronizing to start an active race should only be possible when the clock is connected with the Club antenna (CA). During the time where one or more races are active, it must be impossible to resynchronize the RTC.
- The ET should generatea unique race code for every race started. The race codesmust be printed on the ~~basketinglist and~~ clocking list (read out).
Loft mode:
- A pigeon is clocked when it moves over the loft antenna. When the pigeon's ID is retrieved in the pigeon table a time stamp is added for the concerned pigeon. The time is delivered by the RTC, this time cannot be deleted before the race is cleared.
- It is not allowed to generate time stamps from an external DCF- or GPS-receiver.
- The clocking times shall be derived at any time from the internal RTC, timing from other external sources cannot be used. An exception to this rule is made when continuous clocking is allowed (§ 3.5.2).
- Any change in fancier's data, race data or pigeon data should be impossible as long as one or more races are active. An exception can be made for an emergency linking at basketing if the federation rules allow this.

3.5.2 Continuous clocking (CCS)

Continuous clocking is charged with a high risk factor. Therefore the protocol and software are very important and must meet the following requirements:
- BIOS, software, crypto keys and serial number of a CCS should comply to the same demands as those for the ET (§ 3.5.1).
- Pigeons clocked through a CCS are not clocked by the RTC of the fancier's terminal (ET) but through a secondtimer. The latter is not necessarily synchronized in a club, except when the CCS is a copy-clock (same ET).
- If the CCS is connected to the ET, the critical racing data communication between the ET and the CCS have to be encrypted. The crypto-keys must be stored on both sides so that they are inaccessible for unauthorized users.
- The continuous use of a CCS should never exceed one day (24 hours). When thistime period is exceeded the CCS must destroy its own data.
- Every new session with a CCS shall start with the exchange of identities (serial numbers).From this moment on the CCS shall only work with the ET who started the session. Next the CCS shall synchronize its RTC with the RTC from the ET. Now the CCS is ready to clock the birds.
- Secret codes may never be uploaded from the ET to the CCS. The CCS-clock cannot be synchronized or reset again before the CSS was cleared by the ET who started the session or before the above period expired. All these communications are encrypted.
- The CCS clocks birds with its own RTC or counter and stores the electronic rings with their secret codesand arrival time data.
- Transferring clocking data from the CCS to the ET takes place at endinga continuous clocking session. This data transfer is initiated by the ET and starts with the exchange of identities. If the identity exchange fails, the CCS must destroy its own data. If the identity exchange is successful the RTC of the CCS is compared with the RTC of the ET. The deviation of the CCS cannot be bigger than 2 seconds, otherwise the session is closed and the CCS should show an error code.
- Now data transfer can start, the CCS delivers electronic ID's, secret codes and time info to the ET. The ET corrects the clocking times with the deviation between CCS and ET, time stamps the pigeons with the corrected time, checks the secret codes and clocks accordingly (OK or Not OK). After this data transfer is completed the CCS is reset and ready for another session. Again,criticalracing data communications are encrypted.

3.5.3 Club antenna /-unit (CA)

BIOS, software, crypto keys and serial number of a CA should comply to the same demands as the these for the ET (§ 3.5.1). A CA can be a simple reader/writer or can provide also several connection gates to other peripherals.
The CA must initiate or generate a secret code in the electronic ring at basketing. The CA must double check this secret code and make sure that a pigeon is only basketed when the secret code was changed. The electronic ID can only be sent to the clock if the secret code was double checked. The CA may never accept an ER of which the secret isn't changed. This secret code generation is the most important feature against fraudulent practices. Other important features for CA and ET are covered by the section about the protocol (Section 3.6).

3.6   Procedures in the club

All safety precautions are worthless if noone checks. Clear protocols are required to limit opportunities for fraudsters. Protocols for basketing, read out, result processing and linking are required. Protocols have to be simple and clear, the fanciers who are doing the job are not necessarily highly qualified. Protocols are a guide for them on how to check the basics. Hard- and software can never guarantee 100% proof against fraud, the best options are offered by clear and simple protocols which must be observed strictly.

3.6.1 Basketing

The first and most important step in setting up a pigeon race is basketing. The first important action is synchronizing the master timer (HKW, GPS, master clock). Synchronizing the master timer must be done by a commission (more than one person).
Basketing an individual fancier starts by setting up the race on the fancier's electronic timer (ET) or on the club antenna (CA). When a race is started the ET's RTC has to be synchronized with the club's master timer, if it is the first race in the ET, or by creating the relative time table in the ET when multiple races are going on (§ 3.5.1).
Once the basketing has started, the pigeons are basketed by presenting their electronic ring number (ER) to the CA.The CA together with the ET must double check (not display) the changing of the secret code in the ER.If the secret code isnot changed, the pigeon cannot be basketed.
(!) A fancier may **NEVER** basket his own pigeons, most fraud cases find their origins here.
(!) The basketing committee should check every basketed pigeons federation numberagainst the number displayed by the ET or the CA.
(!) If federations allow that basketed pigeons can be unbasketed, this should be possible within a limited time window, i.e. 5 minutes. Reasons for unbasketing like: wrong federation band number, health condition of the pigeon, wrong pigeon, .. If the pigeon cannot be un-basketed (not allowed by the rules, outside the foreseen time window), the electronic ring should be removed from the pigeon's leg and must be retained by the race committee, the concerned pigeon has to be marked by the basketing commission on both copies (club and fancier) of the basketing list.
(!!!) Fanciers should be kept away from the CA while basketing their own pigeons, i.e. 1 m distance(also 'strange' objects like a suitcase). If this rule was not observed, all pigeons of the concerned fancier should be removed from the race. Most cases of fraud find their origin here.
 (!) Adding or editing pigeons in the pigeon table during basketing should be avoided but is sometimes necessary. If this occurs, a new linking list should be added to the basketing list.
(!) A basketing session is only within a limited time window, after the last pigeon is basketed the session should be closed. Better is when the ET closes the session automatically, i.e. 10 minutes after the last pigeon basketed.
(!) On a basketing list the following items should be shown:
- serial number of the CA used while printing,
- serial number of the ET,
- the unique race code generated by the ET (§ 3.5.1),
- date and time of printing,
- signatures of the fancier and basketing committee,
- license and name of the fancier,
- the ID for the race (minimum 4 char., § 3.4.3),

- time setting when the race was started (§ 3.4.3),
- list of the basketed pigeons with :
    - electronic ring number <8 char>, **NO SECRET CODES** !!!!
    - federation ringnumber of the pigeon,
    - basketing time,
    - eventually: sex, color, nomination, teams, …
- number of pigeons basketed.

(!) After printing the basketing list at least in 2 copies, one copy has to be kept in security by the basketing committee, one copy for the fancier. If the federation's rules require this, more copies can be printed.

(!!!) At **NO** time during basketing, a club-PC may be connected to the CA or any other interface or peripheral connected to the CA. A connected PC is a potential danger because a PC with appropriate software can recordthe secret codes. Even for highly qualified people it is hard to judge in this matter. Therefore,**NO PC CONNECTION** while basketing, until **last bird registered through the Basketing Antenna(!!!!).**

If a PC is necessary, i.e. for race processing or printing, the connection with the PC can only be madeafter closure of the basketing session.

Systems allowing a pc, tablet or mobile phone to be connected during basketing, mustassure one-way communication from ET to PC.Printing a basketing or clocking list (read out) is done from the club-antenna /-unit or directly from the fancier's Electronic Timer.

3.6.2 Read out

When the pigeons are home the ET has to return to the club to print the clocking list.
The first important action is synchronizing the master timer (HKW, GPS, master clock), synchronizing the master timer must be done by a commission (more than one person).

(!) To get (print) a clocking list the ET has to be connected to a CA.

(!) An active race has to be selected on the ET or CA.

(!) The ET's date and time have to compared with the master timer and this time stamp has to be stored in the ET race table (§ 3. 5.1)

(!) Now a clocking list is printed with only the clocked birds of the selected race, if not clocked birds are printed they should be on a separate list.

(!) On a clocking list the following items should be shown:
- serial number of the CA used while printing,
- serial number of the ET,
- the unique race code generated by the ET (§ 3.5.1),
- date and time of printing,
- signatures of the fancier and race committee,
- license and name of the fancier,
- the ID for the race (minimum 4 char § 3.4.3),
- time setting when the race was started (§ 3.4.3),
- time setting of the read out, to evaluate how the clock is running.
- (!!) if the clock is running with more than 2 sec a day deviation, the timing shouldn't be accepted by the race commission (i.e. basketed on Thursday, readout on Saturday, 3 days, max. deviation is 6 sec.).
- number of pigeons basketed,
- number of pigeons clocked.

(!!) Only birds with evaluation set as OK can be accepted, other birds (Not-OK) are disqualified.

At read out the ET/CA can be connected to a pc for automatic result processing, without danger for recording the secret codes.

3.6.3 Evaluation of the clocking list

The race committee has to evaluate every clocking list after it is printed. This takes time, however if one wants to prevent fraud, one can't escape, sampling is the absolute minimum.
What to look for while evaluating?
Compare the basketing list with the clocking list:
- a clocking list without the corresponding basketing list is INVALID,
- race codes should be equal,
- ET's serial number is equal on both lists,
- CA's serial numbers can differ,
- the time setting starting the race must be equal on both lists,
- fancier's license and name are equal on both lists,
- electronic ring numbers are equal on both lists,
- federation ring numbers are equal on both lists,
- number of basketed birds is equal on both lists,
- federation and electronic ring numbers on basketing and clocking lists might be checked against the linking list.

3.6.4 Linking

Linking is the operation where the federation's band numbers are referenced (linked) to the electronic ring ID's. Linking can be described as editing the fancier and pigeons data in the ET. Where one does and who is doing this is of lesser importance. A linking list should be presented to the club each time fancier and pigeon data are edited. It's the fancier's responsibility to present this list when changes were made. He should get a signed copy from the race committee at that moment.
A federation could mention in its rules that linking is only allowed in the club. However doing this doesn't add any additional security dimension regarding security or safety. The identity of the bird has to be checked each time again at basketing, it's the responsibility of the basketing commission.
(!) It makes sense to draft a rule whereby pigeons are disqualified from the race if no correct linking list was presented before basketing.
(!) Linking list has to be printed by the race committee when the ET is connected to a CA.
(!) A linking list should show the following items:
- serial number of the ET,
- serial number of the CA,
- fancier's license and name,
- combination of electronic ring and federation ring,
- eventually: sex, color, …

3.7   **Compatibility**

3.7.1 Club protocols

Several countries demand a minimum compatibility for basketing, read out and linking. To ensure this compatibility, public communication protocols are available.

Unives 1.7 [ref. 2]documents different situations where the ET is the master in a Master-Slave environment at the club.

Unives Belgium documents different situations where the Clubmaster is the master in a Master-Slave environment at the club.

Both have their advantages and disadvantages. They are added to this document in separate documents.

When using such compatibility protocols, it makes no sense to encrypt the communications between the CA and the ET at basketing. One extra reason to follow the basketing protocol very strict.

Updated or newer version of club software should be compatible with existing CA or club unit hardware.

3.7.2 Ring compatibility

It looks good when all ETS's are using the same electronic ring. Important to know is that rings from one brand oftenfunction optimal only on the antennas of the same brand. By obliging one universal ring a federation is giving indirectly advantages to some and disadvantages to others. Also important to know is that ETS companies needs the revenue from ring sales to guarantee their services. Don't forget manufacturers have a very important role in the fight against fraud, they can only keep up by continuously improving their sofwares.

Club antennas mustshow optimal functioning with at least the Hitag-S (or more advanced) chiprings as listed in the attached Chipring Overview.

Chiprings of a new brand of ETS shall be equipped with Hitag-S or more advanced transponder with at least 256 bits memory (192 bits user available).

A chipring shall always reliably transmit data within a distance of up to 5 cm from the CA or loft antenna. A read/write session between chipring and CA shall not last longer than 500ms.

3.8   Homologation procedure

FCI makes an inventory of all available ETS on the market. This can be done in corporation with system manufacturers. ETS manufacturers cooperating with FCI get an FCI certificate of homologation. FCI keeps a list of all approved ETS on the market.

Approval procedure:

- The ETS manufacturer must complete the application form, every document has to be dated and signed by the management of the company (Homologation request ref. 3,4) .

- The application must be accompanied by a hard-ware inventory of the equipment, also dated and signed by the management of the company (System inventory ref. 5).

- The application must be accompanied by the completed list of questions added by FCI, also dated and signed by the management of the company (FCI query ref. 6).

- Software should only be evaluated in general on FCI level (FCI query ref.6). A list of software versions should be presented to each individual national federation. These

federations should impose a rule that results with other than the registered version are invalid. Software versions may differ from one federation to another because there are differences on how pigeon races are organized. The manufactures have to cooperate with National Federations to update software versions in accordance current rules of the Country.
- The application must be accompanied with the documents proving that the equipment on the inventory list carries rightfully a CE-label (measurement reports and statements). This means that the equipment conforms with the essential requirements and with other relevant provisions of the R & TTE directive (1999/5/EC). I.e.: Reader for transponders (125kHz) in agreement with A§3 of R & TTE- the directives: Health and Safety A§3 (1) a (Applied standard: EN 0950: 1992 +A1 +A2: 1993 +A3: 1997 +A11: 1997), EMC §3(1)b: Applied standard: EN 301 489-3/07.200, Radio frequency spectrum A§3 (2) (Applied standard EN 300 330-2 V1.1/7.2000). (CE-label ref.7)
- The applicant must deliver a declaration of responsibility to FCI. This document has to be signed and dated by the management of the company (ref. 8). The manufacturer declares that all given information is correct. The manufacturer declares that in the event of fraud with equipment delivered by them and in the case where all safety protocols were followed by the responsible commissions or committees, they shall take appropriate action by adapting hard- and software in such a way that the incident can't be reproduced in the future.

3.9   Homologation fee

FCI should have the means to detect possible violations. In cases of doubt, FCI must have the means to appeal to independent agencies for testing. To acquire these means, ETSrequesting a FCI-certificate [ref. 9] of homologation must pay a homologation fee of:
- systems on the market before 2011 in the possession of a TNO report, pay a fee of €1.000,- for their complete hardware inventory until 27th January 2011. Manufacturer must certify itsTNO report to the FCI.
- systems on the market before 2011 without a TNO report or Smits Techniek report, must make the report between year 2011-2017. From 2011 the new Evaluations will be done by Smits Techniek and the requests must be sent to FCI. The fee will be stated by contract between FCI and the Testing Agency.
- All the Evaluation fees must be paid to the FCI and includes all costs.

3.10 References

[ref. 1]

H4001 norm (Initiated by Electro Marin,CH), Memory Array

The H4001 contains 64 bits divided in five groups of information. 9 bits are used for the header, 10 row parity bits (P0 - P9), 4 column parity bits (PC0 - PC3), 40 data bits (D00 - D93), and 1 stop bit set to logic 0.

1	1	1	1	1	1	1	1	1	- 9 Bits Header
8 Version bits or Customer ID				D00	D01	D02	D03	P0	- 4 data bits & associated even row parity bit
				D10	D11	D12	D13	P1	
				D20	D21	D22	D23	P2	
				D30	D31	D32	D33	P3	
32 Data Bits allowing 4 billion of combinations				D40	D41	D42	D43	P4	
				D50	D51	D52	D53	P5	
				D60	D61	D62	D63	P6	
				D70	D71	D72	D73	P7	
				D80	D81	D82	D83	P8	
				D90	D91	D92	D93	P9	
				PC0	PC1	PC2	PC3	0	- 4 column even parity bits, NO row parity bit

The header is composed by the 9 first bits which are mask programmed to 111111111. This header is used to recognize the beginning of a new data record. Due to the data and parity organizing, this sequence can't be reproduced in the data string. The header is followed by 10 groups of 4 data bits and 1 even row parity bit. Then, the last group consists of 4 even column parity bits without row parity bit. Bits D00 to D03 and bits D10 to D13 are customer specific identification. These 64 bits are outputted serially in order to control the modulator used to modify the current at one of the coil terminals. When the 64 bits data string is outputted, the output sequence is repeated continuously until power goes off.

[ref. 2]   Unives 1.7   (separate document)
[ref. 3,4]   Homologation request   "
[ref. 5]   System Hardware Inventory   "
[ref. 6]   FCI query (CA, ET and CCS)   "
[ref. 7]   CE-label
              - ec_europa_eu_enterprise_rtte_dir99-5.pdf
[ref. 8]   Declaration of responsibility   "
[ref. 9]   FCI homologation certificate   "
-             Inventory list stamped and signed by FCI   "
-             TNO /SMITS system reports overview   "
-             List of current used chiprings

4. Security requirements forElectronicClocking Systems

CONTENTS

4.1   Physical and hardware security

Attack methods

Physical protection against attacks by tampering with a system component,e.g., a pigeon clock (ET), consists of three components, described in the international ISO-standard: ISO 13491 (parts 1 and 2). According to this standard, physical protection is devided into tamper resistance, tamper response and tamper evidence.

Attacks on the hardware of a system or system component, make use of (a combination of) of attack methods. Physical protection shall offer such attacks a minimum chance on success in reaching critical components and manipulating data. The attack methods and examples of attacks, are shown in the table below.

	Attack method	Examples
a	Mechanical	Removing or opening housings by drilling, grinding, etc.
b	Chemical	Removing housings with solvents or otherwise
c	Electrical	Galvanic tapping, generate electrical interference
d	Magnetic	Blocking a tamper switch with a permanent magnet; Tapping or inducing LF- (inductive) fields up to 150 kHz
e	Electro-magnetic	Analysis of radiated RF-signals above 150 kHz; Affecting data with EM-radiation
f	Optical	Tapping and /or interfere with an IR-channel; Affecting with Laser
g	Thermic	Melting or burning housings; Affecting electronics by heating orcooling

Physical protection

The system components must be designed and constructed such, that an attempt to attack critical components (memory, RTC) which transmit, store or proces data (software, flight data), results in visible, irreparable damage to a system component.

Access (authorized or not) to system- or application software and data, for repair or maintenance, shall only be possible after erasure from memory of all flight- and User data.

Data inputs /outputs of system components must be protected against monitoring, such that – when used in normal home environment – no attacks are likely that may result in compromising software or flight data.

External communication- and power lines of system components must be separated from internal critical data (software, flight data) in such a way that these data cannot be tapped or modified.

Modification or replacement of system components during a flight, which is possibly not detected by logical protection in that component or by the system, must as much as possible be hampered by design of the physical security measures.

Protection layers

System components
The physical protection of data in a system component must at least consist of a rigid housing that cannot be opened by an attacker and closed again, without leaving clearly visible damage to the housing.
The housing of a system component must be sealed or completely filled up with an (epoxy) filler.

Housing
The housing of the User Terminal (Pigeon clock) must be equipped by the manufacturer with a seal. The seal may be placed on the seam of the main housing parts, or on (one or more of) the screws of the housing, in such a way that easy damage of the seal at normal use of the clock, is not likely to occur.
The seal must be specially designed for security purposes and shall not be:
- easy to copy with generally available means;
- replaceable without visible, irrepairable damage to the seal.

The housing with sealing and eventual other protections against fraude (special mounting screws, etc) is considered to be the outer physical protection layer.

In addition to the protection layer described above, the User Terminal shall provide an inner protection layer, which hampers that, after unauthorised opening of the housing, electronics hardware can easily be reached to affect data.

The inner protection layer may consist of a second physical protection of critical data (e.g., electronic components potted in epoxy), or detection of fraud attempts may be obtained by electronics or physics.

The means of detection may, for example, consist of a sensor and electronics. It shall not be possible to:
- reach and put the sensor system out of order from outside;
- put the sensor system out of order with simple means, with open housing.

Fraud detection

If the manufacturer has taken provisions (such as sensors, hard-/software) in User Terminal (pigeon clock) to detect a fraud attempt by generating a fraud (alarm-)signal, this fraud signal shall initiate both the following actions:
a) The signal either causes erasure of flight data or put the User Terminal out of order.
b) The signal shall be stored on an unknown or unspecified (non-volatile) memory location. This memory location shall not be available to the User, when he controls erasure of flight data. The fraud signal can be visible (display) for the User. The fraud signal must clearly be printed when the evaluation list of a race is made in the club.

4.2   Security of network operating ETS

ETS - General

In current ETS, (optional) interfaces in the loft system are applied as add-ons, providing communication gates with several peripherals used by the fancier (printer, pc, mobile phone, internet adapters, …). These interfaces can only be used for transferring data to other device or platforms. They can never be used to modify race data or to give information about secret codes, see also § 3.4.7.

In many countries, large distances between fancier and club ask for facilities offered by a network (UMTS, Internet), providing control and uploading of race data to a network server. ETS are already on the market, using a network for transmission and storage of race data. This option offers online race results (basketing and clocking lists), which can be remote-controlled, processed or evaluated by the fancier via the Internet. More options, using Internet (Time) servers, are already on the market.

ETS - Security

Security in network operating ETS is based on: Authorization, Authentication and Accounting. In the ETS architecture, Authorization and Authentication must be covered by encryption and unique device-ID's. Accounting means, e.g., detecting fraud by software (checks on time intervals, loft antenna location, suspicious arrival times, etc.).

Internet options offer several functionalities and ask for hardware and software interface modules added to the loft system. These are subject to fraud, specifically by experts in the fields of pc-software and data communication. This is the reason why minimum requirements

must be fulfilled to ensure integrity of remote race results for official use and to maintain minimum reliability and availability of the fancier's ET.

An ETS provided with software for network facilities, must still be able to securely clock pigeons and regular strike-off races in the club according the requirements, without necessary use of the network.

In network mode, the time stamps generated in the ET shall be derived from the RTC of the ET. Synchronization may take place with Internet Time, eventually with redundant GPS-time. When Internet Time is used as master time, deviations in the time stamps of the ET and the master time shall be logged. The deviations can be used for building a relative time table (see §3.5.1), keeping track of the deviations of the RTC in the ETagainst the master time. The RTC of the ET cannot be synchronized with an external clock or Internet Time when a race is active.

Network interface hardware or adapters in the loft system are not allowed to perform clocking of pigeons. Temporary storage of clocking data can take place in such interface devices, but these flight data are not for official use. The ET is responsible for the storage of clocking data, for official use when necessary.

All data uploaded from the ET to the network system must be protected such, that the network system is able to detect manipulation of that data. This security function needs an Internet connection, enabling a server to verify authenticity of messages from the loft system.

The interface and hardware between the ET and a network server, must protect stored clocking data in the server against modifications by an unauthorised user of the loft system.

The ET shall provide additional event logging concerning the transmission of data to the network server, such as time of connect, log on, disconnect, … The events shall be stored in the ET such that these data cannot be modified by an unauthorized user.

The ETS with network facility shall always need to basket pigeons in the club on a regular club antenna. On-line facilities of the ETS shall leave the continuous clocking functionalities and physical protections unchanged (§ 3.4.8, 3.5.2).

An ETS using network facilities must apply FCI-certified electronic chiprings. Regular use of Hitag-S transponders or more advanced technologies are preferred.

Reliability

Reliability of network operating systems maybe a problem nowadays. Cyber attacks(criminals) leave software chaos and damages Internet. Internet of Things are vulnerable to attacks which could have negative effect on integrity of flight data communicated via Internet.

The ETS manufacturer must use available tools to prevent compromising flight data, either from direct attacks on the system or from side-effects of cyber-attacks.

Loft system – Interfaces

The manufacturer's webserver for storage and evaluation of race results, is protected by Internet Protocols, considered to offer state-of-the-art security. The loft interface between ET and the network shall provide security, using standard authentication and cryptographic protection. Crypto keys shall be protected when an attempt is made to tamper with the ET or interface module.

The interface to a gateway or the Internet cannot be used to modify race data in the ET, neither to give information about secret codes or effect change of the ET-clock (RTC).

An interface or adapter in a loft system for transmission of data to a network, shall provide physical and logical protection as required in the ET (§ 3.4.3 and 3.5.1). One-way communication from the ET to a network is important.

When the ET is in 'network mode', race- and clocking data is transmitted from the ET to an Internet server, via an Internet router or UMTS to a gateway. During clocking, the ET should preferably stay connected to the network.

When the ET is disconnected from the network, either by the fancier or caused by network failure, the ET must be able to continue clocking and be taken to the club for strike-off, as long as flights are active.

Loft system - Software

An ETS must, automatically or manually, be connected to the Internet, for operating in 'Network mode'. This mode of operation needs the ET be updated with software, providing interfacing and management of data transmission and security.

When the fancier's PC is used for online facilities, only (certified) software from the ETS-manufacturer shall be used, providing secure transport and uploading of data from the ET. Data from the ET should be protected by (standard) available cryptographic measures and data authentication, against attacks on system clocks and clocking data.

5. Requirements forfootrings, electronic combi-rings and chiprings

Contents

Definitions

Introduction

This chapter defines the minimum requirements forquality and performance ofpigeon footrings and combi-rings. The requirements consist of functional- /user requirements, environmental requirements and mechanical properties.
The requirements with respect to programming of the chip-unit of a combi-ring and the chipring are described in chapter 5.2: Electronic chiprings.
The requirements are in principle controllable or testable.For unambiguous results from testings, a test guide based on these requirements is given in a separate document.

Definitions

Footring	A footring is a ring which, once put on a pigeon's leg, serves as an identification of the pigeon, gathered from a clearly readable number, the footring number.
Chipring	A chipring is a ring, provided with a fixed electronic chip which contains extra identification- and user-data that can, in accordance with requirements, be read and written electronically. The chipring may be added to a pigeon's footring, so as to make that pigeon suitable for electronic clocking.
Combi-ring	A combi-ring combines the footring- and chipring-functions by integrating an interchangeable, electronic chipunit with the footring. The chipunit contains the chip with the extra identification- and user-data for electronic clocking.

5.1   Footrings and combi-rings

Contents

5.1.1   Mechanical properties

5.1.2   Performance requirements

5.1.3   Environmental requirements

General

The requirements described in paragraph 5.1.1: Mechanical properties, are valid for footrings and combi-rings, equipped with a metal inner ring.

The quality- and environmental requirements described in, respectively,paragraphs5.1.2 and 5.1.3 are valid for footrings and combi-rings in general, independent of construction or composition of the ring (e.g., a combi-ring with different inner ring).

5.1.1   Mechanicalproperties

General

In the requirements of this paragraph, it is considered that, for the purpose of rigidness, a footringmay be equipped with ametal (e.g., aluminium) inner ring. The coating over the inner ring must contain the necessary printed sticker (with unique ring number) and a transparent, plastic outer ring.

The quality- and environmental requirementsdescribed in, respectively,paragraphs5.1.2 and 5.1.3 are valid for footrings and combi-rings in general, independent of construction or composition of the ring.

Footring and combi-ring

For the purpose of standardization and compatibility with different chip units and clips, the mechanical properties as listed below, are valid.

Inner ring dimensions
-    Inner diameter: fully circular, diam. 8,0 +/- 0,1 mm
-    Thickness: .2 to .3 mm (the combi-ring: see 1.2)

Stickerdimensions
-    Height / width of 7.2 +/- .1 mm
-    Symmetrically positioned between the rims of the inner ring

Sticker readability

- Good contrast between the sticker and printing, for good readability
- Text size: 4 to 5 mm. Colour: black,reproducing style (font, spaces, etc.), constant over a calendar year
- No contrast decrease at lasting exposure to daylight.

Inner ring finishing
- Smooth inner finishing
- No sharp edges inside or outside
- Top- and bottom side finished with an bended rim

Plastic coating properties
- Constant colour shade within a calendar year
- Constant thickness around the ring
- Good transparancy around the ring
- Good resistance against (splash)water
- Good resistance against bending and scratching

Composed ring properties
- Constant outer diameter around the ring
- Height / width: between 10.0 and 10.5 mm
- Resistant against distortion by bumping and mechanical pressure
- No sharpedges
- Weight: maximum 2 gram (without chip unit)

Additional requirements combi-ring

Combi-ring application
- The combi-ring must be suited to be used with the different chips, which are applied in electronic systems and are constructed as a chip-unit, compatible with the combi-ring.

Chipholder
- The chipholder of the combi-ring must be constructed so that a chipunit can be put into the chipholder from both sides, whereby the chipunit always remains firmly in its correct position in the chipholder.
- The chipholder of the combi-ring must not show sharp protuberances, which may somehow hamper or hurt the pigeon.

Chipunit
- As a tactilerecognition that the chipunit is placed correctly, it must provide a mechanical stop. Preferably, an extra 'click', preventing that, under extreme conditions, the chipunit by itself comes loose from the chipholder.

Inner ring
The metal inner ring thickness is between .30 and .35 mm.

5.1.2   Performancerequirements

General

Where requirements in this paragraph refer to normal conditions, these conditions are:
- environmental temperature of 23 +/- 3°C
- relative humidity between 40 and 60%

Footring and combi-ring

Functional lifetime
The ring must remain in good condition for 15 years, under normal conditions and by normal use (according manufacturer's directions), i.e., not be severely damaged or affected, leaving the outer and inner surface of the ring smooth and the text on the sticker clearly readable.

Impact resistance
The impact resistance of the coating must be according DIN 53453 at 23°C: not breakable (or ASTM D 2794, ASTM D 5171).

Abrasion resistance
In order to prevent poor text readability in time, the plastic coating should offer sufficient abrasion resistance (D 4060-07, ISO 7784-2).

Tensile strength
The ring shall withstand a tensile pressure of 20 kgf in any direction, without showing permanent distortion.

Vibration and shock
The ring must remain undamaged at:
- constant vibration of 1g at frequencies between 10 and 1000 Hz
- shocks up to 100g.

Additional requirements combi-ring

Placing the chip
Placing the chip in the chipholder has to be done manually and easily, without the need for tools. Preferably, an extra 'click' mechanism is added, preventing that, under extreme conditions, the chipunit by itself comes loose from the chipholder.

The chipholderconstruction
- The chipholder must be integrated in the ring, such that it cannot be removed (e.g., when no chip is used) or come loose from the ring, e.g., by bumping.
- The chipholder must reproducibly be manufactured in a way that, if necessary, the chip can be placed or removed, always according to the directions for use.

The chipholder must be constructed so that the chip cannot come loose, caused by:
- constant vibration of 1g with frequencies between 10 and 1000 Hz
- shocks of 100g.

The chipholder availability
- The chipholder and chipunit must, by normal conditions and normal use, remain undamaged and unaffected so that the chip remains easily replaceable and will not come loose from the chipholder, e.g. when bumped.

The chipholder keeps its required user properties, when a chip has been placed and removed at least 5000 times.

5.1.3 Environmentalrequirements

Storage
The footring must meet the quality requirements after continuous storage at temperatures between -25°C en + 70°C.

Functionality
The footring must meet the quality requirements at environmental temperatures between -5°C en + 55°C, unless otherwise specified in the requirement.

Humidity resistance
The water absorption of the plastic shall not exceed 1%.
By normal environmental temperature and normal use, the footring shall resist a continuous relative humidity of 95%.

Sunlight
Continuous exposure to sunlight shall have no negative affect on the ring properties.

Chemical pollution
Continuous exposure to small concentrations (up to 10%) of ammonium, salts, acids or alkalics, shall have no negative affect on the ring properties.

Additional requirements combi-ring

Immunity to dust

The functional properties of the chipholderand the chipunit (insertion, ease of use) shall not negatively be affected by dust.

5.2   General requirements for electronic chiprings

Contents

1.   Data and memory programming

Performance and memory programming of the chip for pigeon fancier application, must fulfill the requirements of this paragraph.

The user available memory of the chip comprises at least 128 Bits

The first memory section (1st page) is reserved for data that cannot be modified during lifetime of the chip. In this memory, a fixed chipring-ID and an initial (manufacturer-) identification code (Custom-ID) are written once. For pigeon fancier application, a variable (secret) number of at least 8 Bits must be written often. At power-up it must be possible to read this memory section automatically, before the chip is ready for receiving further read/write instructions.
The first memory section contains the following data:
- Chipring number
- Manufacturer-ID (OEM-code, Custom-ID)
- Variable (secret) number
+ Start-Bits and Parity-Bits.

This first memory section shall be arranged according to the, in paragraph 3.2: 'Data structure' described structure (H4001 structure, see "Electronic Pigeon Ring Specifications")

The remaining memory sections preferably provides a R/W (read-/writeable) section for user data and a section that can initally be configured to OTP, making this part writeable only once. The memory capacity of these pages must be sufficient to contain the following data:
- Country (manfacturer) number
- Fancier-ID data
- Space for future use
+ CRC-Bits.
The chip must provide for security measures to assure the integrity of user data:

- Possibility to set at least 2 pages OTP (by Lockbits). The configuration lockbits must be set R/O, the memory lockbits must be set OTP.
- Possibility of the chip to check the authenticity of the reader (antenna) and to contain a unique identifier.
- The chip must provide storage of a pseudo-random number, which can be used for the authentication of a pigeon (chipring) in a certain race.
- Crypto-possibility for the encipherment of data.

2.   Physical properties

Physical and electrical properties of the chipring must fulfill the requirements of this paragraph.

The resonance frequency of the input detection circuit must be 125 kHz +/- 6 kHz.

The voltage supply for the chip must be attained from the 125 kHz carrier of a RWD / antenna (Read/Write Device). Power-up of the chip must take place at sufficient field strength.

The transponder must be capable of bi-directional transmission, i.e., receive and transmit through the inductive channel (125 kHz).

EEPROM cells are vulnerable to direct U.V.-light and Gamma-radiation (Röntgen). These radiations can erase the contents of the EEPROM-cells (data bit). Therefore, the contents of the user-EEPROM, but also the dedicated (non-user) EEPROM part (configuration, random nr.) of the chip must be protected from these sunlight radiations. For that reason, the chipring must be made of a (ultra-)light absorbing plastic, protecting the chip from being affected by U.V.-radiation that can normally be expected from sunlight.

Also after removing the transponder from the chipring, deliberate erasure of the memory with U.V. (for example, with a EPROM-eraser) must not be easy to perform.

The transponder with the chip must rigidly be enclosed in the plastic of the chipring (chipholder). Normally available chemicals (such as salt) should not reach or affect the transponder.
An attempt to remove the transponderchip from the chipholder, must result in visible damage to the ring or chipholder and, if possible, damage to the chip.

5.3   Chipring specifications

Contents

General

This chapter describes the technical requirements and performance characteristics that electronic pigeon rings need to satisfy in order to ensure quality of the pigeon rings and accountability with the accepted electronic clocking systems.

The electronic pigeon ring is a data carrier for the identification of pigeons. It consists of a lockable ring body in which some space is reserved for the inclusion of electronics. The electronics consist of a programmable memory chip and a connected coil, which together constitute what is known as a transponder. The data that this transponder contains can be read with reading systems and can be adjusted with writing/reading systems.

The only electronic pigeon rings accepted are those that are compatible with the existing and approved reading devices.

Transponders for pigeon rings must comply with the following standard performance characteristics and data programming, specified in this chapter.

5.3.1 Performance characteristics

a. Reading function

Resonance frequency: 125 kHz +/- 6 kHz at normal ambient temperature
Read-out information: 64 bits cyclic, without pause
Data header: 9 bits "1"
Modulation mode: ASK or load modulation
Signal coding: Manchester method
Processing capacity: 125kHz/64 = 2 k baud
Transmission time: 40 ms max. after switching-on sensing field
Reading distance: 8 cm (Measuring structure and measuring specifications are defined in the "Test methods" directive)

b. Writing function

Resonance frequency: 125 kHz +/- 6 kHz at ambient temperature
Modulation mode: ASK
Performance data:

- Writing distance: 3 cm minimum (measuring structure and measuring specifications are defined in the "Test methods" directive)

- Minimal permissible writing cycles: 10,000

- Minimal maintainability of the data: 10 years at 0 - 50°C

- Write protection: the chip should have a protection function against unauthorised or unintended modification of specific data.

- ESD voltage: 10 kV min.

- Insusceptibility to interference: no unintended changes in the data should occur in the transponder's range of application.

There is a special directive for the methods and devices applied in the test procedures.

5.3.2 Data structure / data contents

In correspondance with the data structure, different sections are provided which may contain different data contents.

The first page contains data that should not be altered during the whole life-span of the ring. Additionally, a variable, secret number is stored in this page.

The second and next pages contain varying data, which means that these are read/write suitable. The pages can be used for extra information. They are reserved for the identification of the user.

The following data, especially described with reference to the storage capacity, characterize the necessary requirements.

The table below shows the data structure for the first 64 Bit of the chip:

Bit 0	1	1	1	1	1	1	1	1	1	Bit 8		Header
				Bit 9	D00	D01	D02	D03	P0	Bit 13		Suppliers ID
				Bit 14	D10	D11	D12	D13	P1	Bit 18		(= Customer ID)
				Bit 19	D20	D21	D22	D23	P2	Bit 23		
				Bit 24	D30	D31	D32	D33	P3	Bit 28		
				Bit 29	D40	D41	D42	D43	P4	Bit 33		
				Bit 34	D50	D51	D52	D53	P5	Bit 38		Electronic
				Bit 39	D60	D61	D62	D63	P6	Bit 43		ring number
				Bit 44	D70	D71	D72	D73	P7	Bit 48		
				Bit 49	D80	D81	D82	D83	P8	Bit 53		Secret number
				Bit 54	D90	D91	D92	D93	P9	Bit 58		
				Bit 59	PC0	PC1	PC2	PC3	0	Bit 63		Column parity bits

The parity bits P0 … P9 form each time an even parity with respect to the corresponding line. The same goes for the column parity bits PC0 … PC3, where the header bits are not taken into account. The bits DO0 … D13 indicate the suppliers ID. The bits D20 … D73 contain the electronic ring number that is allocated only once. The bits D80 … D93 contain the secret number.

After power-up, the 64 data-bits of the first page are sent off cyclically. The second page is sent only on request.

It must be possible to change the variable data range (starting at page 2). This can be achieved by a read-write antenna as defined in the test method.

The data structure of the second page (containing 64 bits) is shown in the table below:

	Bit 0	L00	L01	L02	L03	L04	L05	L06	L07	L08	L09	Bit 9		Country
Bit 10	Z00	Z01	Z02	Z03	...	...	Z29	Z30	Z31	Z32	Z33	Bit 43		Fanciers ID Unused
							Bit 44	U00	U01	U02	U03	Bit 47		Unused
			Bit 48	R00	R01	R02	R03	R04	R05	R06	R07	Bit 55		Reserve
			Bit 56	P00	P01	P02	P03	P04	P05	P06	P07	Bit 63		Checksum

The position values of the 64-Bit data fields are represented in the table below.

Field	Content	Bits	Size	Value span	Code
Country	International access number	0 - 9	10 bits	001 - 999	binary
Fanciers-ID	Identification of pigeon fancier	10 -43	34 bits	0 - 9999999999	binary
Unused	Free for future use	44 - 47	04 bits	0 - 15	binary
Reserve	Reserved for future use	48 - 55	08 bits	0 - 255	binary
Checksum	8 bits CCITT- CRC	55 - 63	08 bits	0 - 255	binary

Country (bit 0 - 9):

To differentiate between countries, the international access number is used and binary coded. The table below represents examples of 'Landcode' values.

Country	Access number	Value
Germany	049	031 hex
Netherlands	031	01F hex
Belgium	032	020 hex
France	033	021 hex

Fanciers-ID (bit 10 - 43):

Of the pigeon fanciers ID, only the numerical data without separator are binary coded. The values are represented by non-significant noughts.The table below represents examples of Fancier-ID values.

Country	Fanciers ID	Numerical value
Germany	123/45 67.890	0 49 96 02 D2 hex
Netherlands	1234 - 5678	0 00 BC 61 4E hex
Belgium	012345 - 67	0 00 12 D6 87 hex

Unused (bits 44-47):

free for future use

Reserve(bits 48-63):

reserved for future use

Checksum (bits 56-63):

An 8-bit CCITT-CRC is used to protect the data. 1C3hex serves as a generator polynominal. The CRC is calculated and inverted by means of the 7 data bits, from bit 0 to bit 55, and stored by bits 56 to 63, where bit 56 is the MSB and bit 63 the LSB. The result of the inverted storage of the CRC is that a sequence of noughts is recognised as invalid.

The table below shows the CRC for a number of test bytes:

Byte	CRC
01 hex	C3 hex
80 hex	EE hex

Abbreviations:

CCITT: Comité Consultatif International Téléphonique et Télégraphique

CRC: Cyclic Redundancy Check

MSB: Most Significant Bit

LSB: Least Significant Bit

The memory distribution of the second page of the chip has been chosen in such a way that an optional coding of the data is possible in this page.

5.3.3   Suppliers ID

The suppliers or customer ID is a special section on the 64-bit code of the first page, which makes it possible to differentiate between different suppliers/customers and applications (see Table 1 in Section 3.2). FCI manages these number cycles and informs the requesting manufacturer about what should be written in this section of the chip (= suppliers ID). This manufacturer must also take the responsibility of coding each electronic ring number only once within the suppliers ID allocated to him. (This means that, at maximum, 2^{24} = 16.777.216 rings can be coded).

The agreed Custom-ID's for chiprings are listed in a separate table, see Annex 3.

5.3.4   Data integrity

The access to the data on the chip should be protected and controlled by means of a protection system to prevent undesired and/or unintended changes in the data. For this purpose three procedures have been approved, which distinguish themselves by different protection phases:

1) Definitive structuring of the page by OTP (one-time programmable)
2) Protection by means of a password, preventing unintended or coincidental replacement (e.g. due to strong fields).
3) Storage of a secret number, generated during basketing by an internal or external a-select number generator.
4) A cryptographic procedure whereby the data to be recorded are stored in the memory encrypted.

5.3.5   Mechanical characteristics

Ring dimensions:

- inside diameter 8.5 mm min.
- height: 16 mm max.
- thickness: 1 mm max.
- weight: 2 gr max.

Ring characteristic: multi-lock.

5.3.6 Material properties

The information below refers to the entire transponder in a plastic package:

a)   Material: Luran plastic or POM
b)   Temperature:
   * for storage and transport: -25°C - +55°C
   * for operation: -5°C - +55°C

c) Impact resistance in accordance with DIN 53453 at +23°C: not broken
d) Resistance to:

   * water: water absorption 1% max.
   * dirt: no detrimental effect of operation
   * water-born table salt solution: a 10% concentration over a 24-hour period at a temperature of 50°C
   * ammonia solution: a 10% concentration over a 24-hour period at a temperature of 50°C
   * UV radiation (direct sunlight): with 20 W, 10° halogen lamp in 30 cm
   * vibration: 0.5 g within 1-1000 Hz range during 1 hour
   * free fall: from a height of 1.2 m

The conditions for testing to minimum requirements will be laid down in a separate directive.

5.3.7 Inscription / Characterisation of the rings

It should be possible to provide the rings with an unambiguous identity by means of an inscription. This identity is laid down in the approval.

5.4   Functional compatibility of the rings with the accepted systems

Apart from testing the ring characteristics against the specifications inthe directive (chapter 5.3), official approval of an electronic chipring also requires the successful completion of a number of function tests with respect to the compatibility between various approved systems used by pigeon fanciers. In addition, a separate directive describes the test-methods and applied tools.

5.5   Acceptance of the electronic chiprings

The procedures for acceptance of a certain electronic chipring are initiated at the request of the chipring manufacturer. Acceptance depends on whether the electronic ring has stood the test that it was subjected to. The associations determine the test methods to be used. A testing agency appointed by the associations will decide whether an electronic ring is accepted. The costs involved will be borne by the manufacturer.

The ring manufacturer must submit a test model and should also file extensive technical documentation with the testing agency. The testing agency will keep a copy of the test results and the test report.

5.6   Additional model licence in case of deviation from test model

Deviations from the test model require additional acceptance by the testing agency.

5.7   Withdrawal of the licence

Deviations from the test model imply necessary additional testing by the testing agency.

5.8   Procedure for the implementation and amendment of this directive and area of validity

This directive will be effected by the committees appointed by the associations for this purpose. Amendments to this directive may be determined by a joint committee to be appointed by the associations.

This directive applies to all land committees who are members of the FCI.

Annex 1 Draft electronic chipring – Test methods

Allgemeines

Diese Richtlinie beschreibt die Testmethoden für elektronische Taubenringe in ErgänzungzudemSpezifikationsschema Elektronischer Taubenring. Ziel dieser Richtlinie ist es durchgenaue Definition reproduzierbareundvergleichbareErgebnissezuerhalten.

NormativeReferenzen

Grundlagefürdiese Richtlinie ist die von den Taubenverbändenverabschiedete Richtlinie „ Elektronischer TaubenringSpezifikationsschema" vom 12.05.2000.

Weitere Internationale Standards

IEC 61000-4-2:1995 Electromagnetic compatibility (EMC) Part 4: Testing and measurement techniques-
Clause 2: Electrostatic discharge immunity test

IEC 68 Teil 2: GrundlegendeUmweltprüfverfahren

Definitionen

Funktionsbeständigkeit: UneingeschränkteFunktion als Taubenring. Dieseumfaßt die elektrische Funktion (Beschreiben, VeränderenundLesen des Dateninhaltes) und die mechanische Funktion.

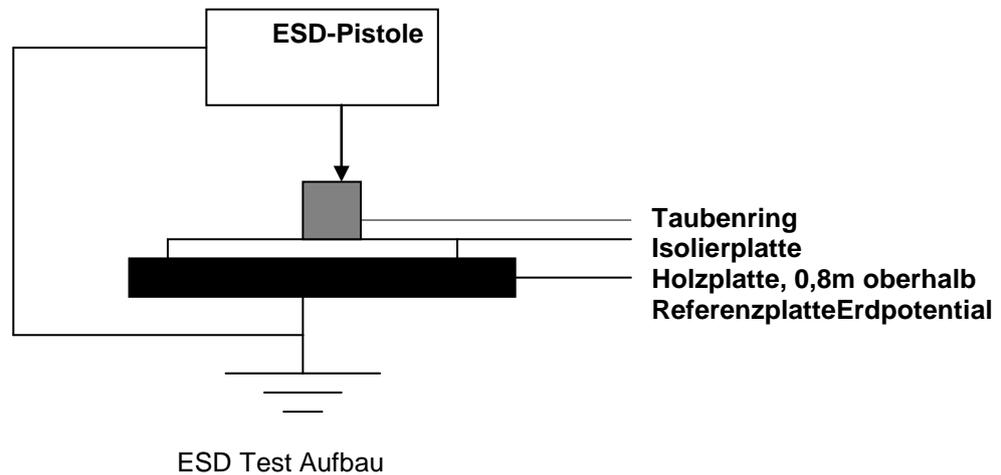Standardbedingungenfür die Testmethoden

Umgebungsbedingungen

Wenn nicht anders spezifiziert, werden die Tests bei einerUmgebungstemperaturvon 23°C +/- 3 °C undeinerrelativenLuftfeuchtigkeitvon 40% bis 60% durchgeführt.

Toleranzwerte

Wenn nicht anders spezifiziert, sind alle Werte, die die Eigenschaft des Testequipmentsund das Verfahren der Testmethoden spezifizieren, mit +/-5% Toleranzbehaftet.

Elektrostatik

Sinndieses Testes ist es, den Einfluß der ElektostatischenEntladungauf den Taubenringzuprüfen, basierendaufdem Human Body Model.
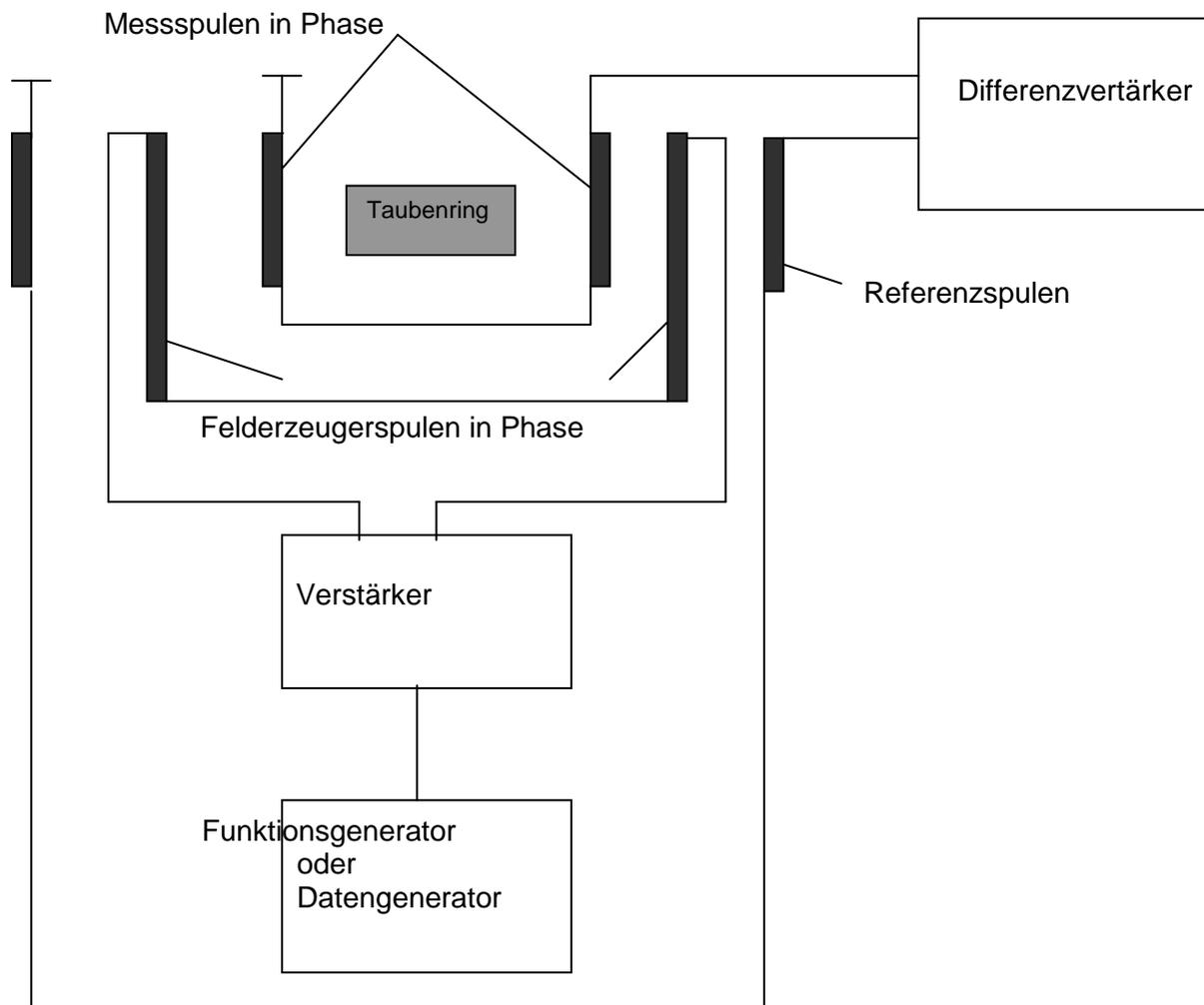


```
                    ┌─────────────────┐
                    │   ESD-Pistole   │
                    └─────────────────┘
```

Taubenring
Isolierplatte
Holzplatte, 0,8m oberhalb
ReferenzplatteErdpotential

ESD Test Aufbau

Testablauf

Grundlagefür den Testablaufist die IEC61000-4-2: 1995
Bei diesem Test sindjeweilsfünfEntladungsvorgängeanfünfverschiedenenPunkten des TaubenringesmitSchärfegrad 4durchzuführen. Zwischen den einzelnenEntladungsvorgängensindmindestens 10sec Zeitabstandeinzuhalten.

Nachdem Test ist die Funktionalität des Taubenringeszuüberprüfen.

TestaufbauzurPrüfung der Leseund der Schreibreichweite

ZurÜberprüfung der Lese- und der SchreibreichweitewirdeinMessaufbaunachdemHelmholtzPrinzipverwendet. Dies ermöglichteinhomogenesMagnetfeld.

Messspulen in Phase

Differenzvertärker

Taubenring

Referenzspulen

Felderzeugerspulen in Phase

Verstärker

Funktionsgenerator
oder
Datengenerator

Der Funktionsgeneratortreibt die Felderzeugerspulen, sodaßdas magnetische Feld in FrequenzundFeldstärkeeinstellbarist.

Messspulen
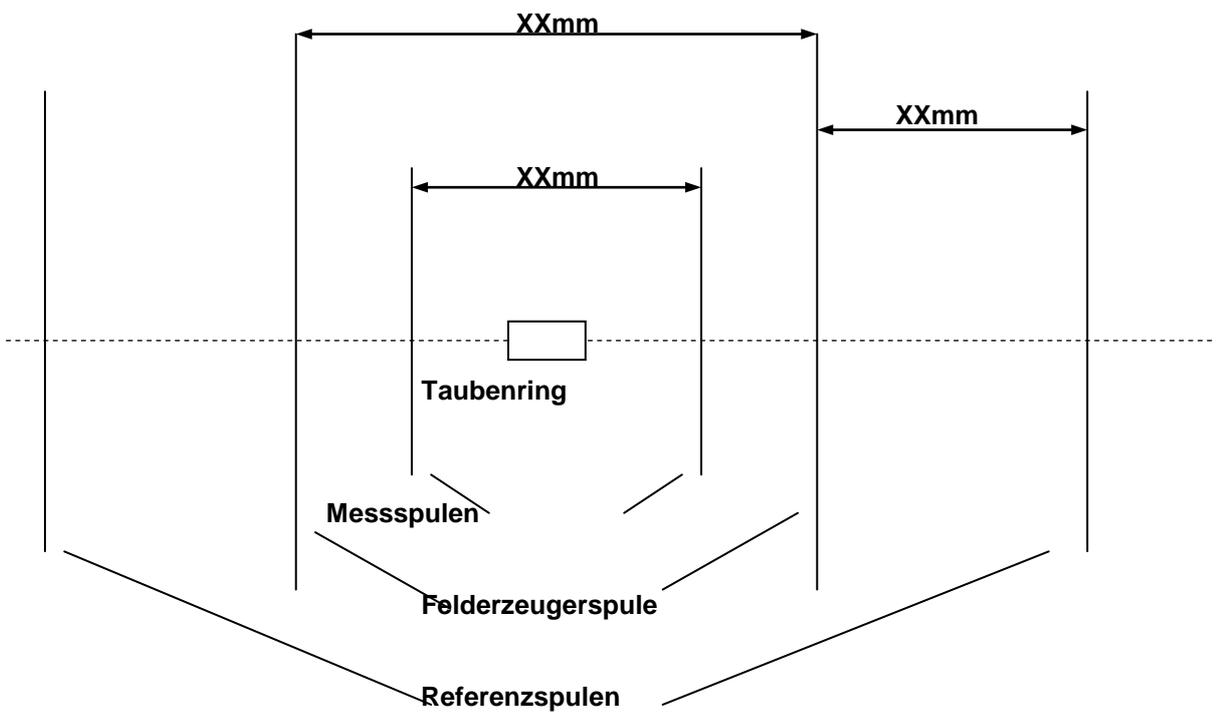Definition der Spulen in mechanischenAbmassenundelektrischen Daten.

Felderzeugerspule
Definition der Spulen in mechanischenAbmassenundelektrischen Daten.

Referenzspulen
Definition der Spulen in mechanischenAbmassenundelektrischen Daten.

Testanordnung

XXmm

XXmm

XXmm
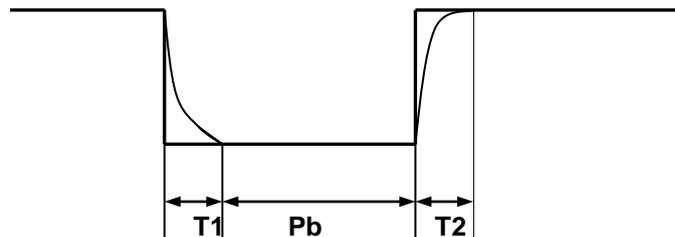
Taubenring

Messspulen

Felderzeugerspule

Referenzspulen

Prüfung der Lesereichweite

Der Taubenringmuß in der Testanordnungzwischen den FeldstärkenHmin=?? UndHmax =?? gelesen werden können.

Prüfung der Schreibreichweite

ZurÜberprüfung der Schreibreichweite des Taubenringesmußsowohl der Bereich der Geheimzahl als auch der Bereich der weiterenSeiten des Taubenringesbeschrieben werden. Die dafürnotwendigen Daten werden voneinemDatengeneratorerzeugt. Das Verifizieren des Dateninhalteskannmiteiner separaten Lesestationerfolgen.

BeimBeschreiben des Taubenringessindfolgende Parameter gegeben

- FeldstärkeH$_{min}$ =??? undH$_{max}$=??
- Geometrie der Spulen
- Pulsform (T1 und T2) undPulsbreite (Pb) der Schreibimpulse
- Modulationstiefe m

Materialeigenschaften des Ringes

Funktionsbeständigkeitgegen Wasser

Der Taubenringwirdvollflächig in destilliertem Wasser übereinenZeitraumvon 10 Tagengelagert. Die WasseraufnahmedarfnachdiesemZeitraum nicht mehr als 1% betragen. Die Funktiondarf nicht beeinträchtigt werden.

FunktionsbeständigkeitgegenwässerigeKochsalzlösung

Der Taubenringwirdvollflächig in eine 10% KochsalzlösungübereinenZeitraumvon 24 Stunden bei einerTemperaturvon 50°C gelagert. Die LösungsaufnahmedarfnachdiesemZeitraum nicht mehr als 1% betragen.

FunktionsbeständigkeitgegenAmoniaklösung

Der Taubenringwirdvollflächig in eine 10% AmoniumhydroxidlösungübereinenZeitraumvon 24 Stunden bei einerTemperaturvon 50°C gelagert. Es darfkeineZersetzungstattfinden.

Funktionsbeständigkeitgegen UV-Strahlung

Der TaubenringwirdmiteinerHalogenlampe in einemAbstandvon 30 cm übereinenZeitraumvon 1 Stundebestrahlt. Die Halogenlampemußfolgendetechnischen Daten erfüllen:

- 10° Abstrahlwinkel
- 20 W Leistung

Der Dateninhalt des Taubenringesdarfsich nicht verändern.

FunktionsbeständigkeitgegenVibration

Dieser Test wirdnach IEC68 Teil 2-34 - Vibrationdurchgeführt.
Die mechanischen Eigenschaften des Taubenringesdürfensich nicht verändern.

FunktionsbeständigkeitgegenFreierFall

Dieser Test wirdnach IEC68 Teil 2-32 - Frei Fallen durchgeführt.

StahlstangeDurchmesser 5mm

Stahlplatte 100 x 100 x 5 in mm
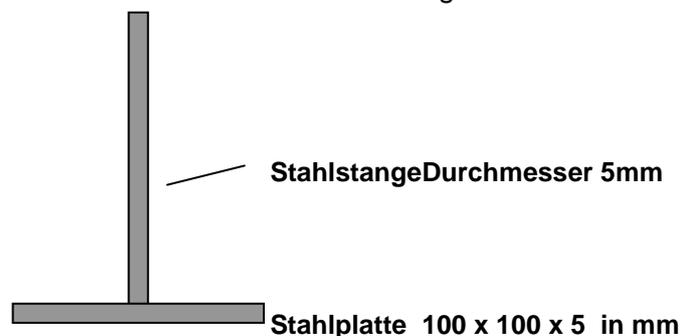
Der Taubenringwirdjeweilsfünf Mal in beiden PositionenauseinerHöhevon 1,2m über die Stahlstangeauf die Stahlplattefallen gelassen.

Nachdem Test ist die Funktionalität des Taubenringeszuüberprüfen.

LagertemperaturundBetriebstemperatur

Dieser Test wirdnach IEC68 Teil 2-24-Temperaturwechsel durchgeführt.

- Temperaturbereich -25 tot +70°C
- Je 3 Std bei -25 en +70°C
- Gesamtdauer min. 24 Std.
- Temperaturänderungsgradient 2,5°C/min.

Die mechanischen Eigenschaften des Taubenringesdürfensich nicht verändern. Der Dateninhalt des Taubenringesdarfsich nicht verändern. Die Funktion des Taubenringesdarf nicht beeinträchtigt werden.

Annex 2 Chiprings overview